

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT NO.: 6,895,420 GRANT DATE: May 17, 2005
INVENTOR(S): Roy R. Faget ATTY. DOCKET NO.: 10001840-1
SERIAL NO.: 09/505,382 CONFIRMATION NO.: 6474
FILED: February 16, 2000
TITLE: APPARATUS AND METHOD FOR SHARING
DATA FET FOR A FOUR-WAY MULTIPLEXER

**REQUEST FOR ISSUANCE OF
CERTIFICATE OF CORRECTION**

**COMMISSIONER FOR PATENTS
ALEXANDRIA, VA 22313-1450**

Sir:

The assignee of the entire interest in the above-identified patent respectfully requests that a Certificate of Correction be issued in accordance with 37 CFR 1.322.

Enclosed herewith is a complete copy of Form PTO 1050 (Rev. 3-82) describing the various errors involved in the above patent. The errors that occurred under Rule 322, appear in Column and Line format.

Issuance of a Certificate of Correction to correct these errors is therefore respectfully requested.

We shall appreciate your returning to us one executed copy of this Certificate of Correction for attachment to our Letters patent. ↗

Respectfully submitted,

By

William P. O'Meara, Esq.

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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APPLICATION NO.: 09/505,382

ISSUE DATE : May 17, 2005

INVENTOR(S) : Roy R. Faget

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE CLAIMS

Claim 1, Column 6, line19, after "dual" insert --rail--

Claim 9, Column 7, line 2, delete "transistor" and insert therefor --field effect transistor (FET)

Claim 9, Column 7, line 19, after "data transistors required" insert --wherein each of the logic gates provides complementary outputs as the shifted data output--

Claim 15, Column 8, line 16, delete "data to" and insert therefor --data from the shared data lines into--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.